

STP6NB80 STP6NB80FP

N - CHANNEL 800V - 1.6 Ω - 5.7A - TO-220/TO-220FP PowerMESH MOSFET

PRELIMINARY DATA

TYPE	V _{DSS}	R _{DS(on)}	ΙD
STP6NB80	800 V	< 1.9 Ω	5.7 A
STP6NB80FP	800 V	< 1.9 Ω	5.7 A

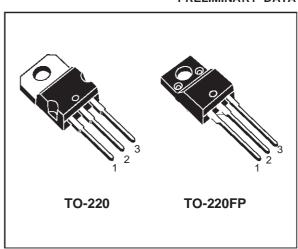
- TYPICAL $R_{DS(on)} = 1.6 \Omega$
- EXTREMELY HIGH dv/dt CAPABILITY
- 100% AVALANCHE TESTED
- VERY LOW INTRINSIC CAPACITANCES
- GATE CHARGE MINIMIZED

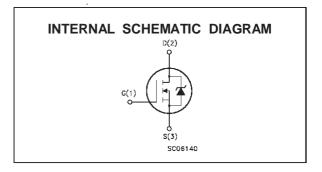
DESCRIPTION

Using the latest high voltage MESH OVERLAY^{IM} process, STMicroelectronics has designed an advanced family of power MOSFETs with outstanding performances. The new patent pending strip layout coupled with the Company's proprietary edge termination structure, gives the lowest RDS(on) per area, exceptional avalanche and dv/dt capabilities and unrivalled gate charge and switching characteristics.

APPLICATIONS

- HIGH CURRENT, HIGH SPEED SWITCHING
- SWITCH MODE POWER SUPPLIES (SMPS)
- DC-AC CONVERTERS FOR WELDING EQUIPMENT AND UNINTERRUPTIBLE POWER SUPPLIES AND MOTOR DRIVE





ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Va	lue	Unit
		STP6NB80	STP6NB80FP	
V _{DS}	Drain-source Voltage (V _{GS} = 0)	80	00	V
V_{DGR}	Drain- gate Voltage ($R_{GS} = 20 \text{ k}\Omega$)	80	00	V
V_{GS}	Gate-source Voltage	±	30	V
ID	Drain Current (continuous) at T _c = 25 °C	5.7	5.7(*)	Α
I _D	Drain Current (continuous) at T _c = 100 °C	3.6	2	Α
I _{DM} (•)	Drain Current (pulsed)	22.8	22.8	Α
P _{tot}	Total Dissipation at T _c = 25 °C	125	40	W
	Derating Factor	1.0	0.32	W/°C
dv/dt(1)	Peak Diode Recovery voltage slope	4	4	V/ns
V _{ISO}	Insulation Withstand Voltage (DC)	2000		V
T _{stg}	Storage Temperature	-65 to 150		°C
Tj	Max. Operating Junction Temperature	15	150	

^(•) Pulse width limited by safe operating area

(*) Limited only maximum temperature allowed

(1) $I_{SD} \le 5.76~A$, $di/dt \le 200~A/\mu s$, $V_{DD} \le V_{(BR)DSS}$, $Tj \le T_{JMAX}$

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THERMAL DATA

			TO-220	TO220-FP	
R _{thj-case}	Thermal Resistance Junction-case	Max	1.0	3.1	°C/W
,	Thermal Resistance Junction-ambient Thermal Resistance Case-sink	Max Typ	62 0.	-	°C/W °C/W
1110 011111	Maximum Lead Temperature For Soldering Po	7 1	30	_	°C

AVALANCHE CHARACTERISTICS

Symbol	Parameter	Max Value	Unit
I _{AR}	Avalanche Current, Repetitive or Not-Repetitive (pulse width limited by T _j max)	5.7	Α
	Single Pulse Avalanche Energy (starting $T_j = 25$ °C, $I_D = I_{AR}$, $V_{DD} = 50$ V)	314	mJ

ELECTRICAL CHARACTERISTICS ($T_{case} = 25$ ^{o}C unless otherwise specified)

OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{(BR)DSS}	Drain-source Breakdown Voltage	$I_D = 250 \ \mu A$ $V_{GS} = 0$	800			V
I _{DSS}	Zero Gate Voltage Drain Current (V _{GS} = 0)	$V_{DS} = Max Rating$ $V_{DS} = Max Rating$ $T_c = 125 ^{\circ}C$			1 50	μΑ μΑ
I _{GSS}	Gate-body Leakage Current (V _{DS} = 0)	$V_{GS} = \pm 30 \text{ V}$			± 100	nA

ON (*)

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}$ $I_D = 250 \mu A$	3	4	5	V
R _{DS(on)}	Static Drain-source On Resistance	$V_{GS} = 10V$ $I_D = 3$ A		1.6	1.9	Ω
I _{D(on)}	On State Drain Current	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $V_{GS} = 10 \text{ V}$	5.7			А

DYNAMIC

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
gfs (*)	Forward Transconductance	$V_{DS} > I_{D(on)} \times R_{DS(on)max}$ $I_{D} = 3 \text{ A}$	2.5	4.5		S
C _{iss} C _{oss} C _{rss}	Input Capacitance Output Capacitance Reverse Transfer Capacitance	$V_{DS} = 25 \text{ V}$ f = 1 MHz $V_{GS} = 0$		1250 145 16	1625 190 21	pF pF pF

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ELECTRICAL CHARACTERISTICS (continued)

SWITCHING ON

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{d(on)} t _r	Turn-on Time Rise Time	$V_{DD} = 400 \text{ V}$ $I_{D} = 3 \text{ A}$ $R_{G} = 4.7 \Omega$ $V_{GS} = 10 \text{ V}$		19 9	27 13	ns ns
Q _g Q _{gs} Q _{gd}	Total Gate Charge Gate-Source Charge Gate-Drain Charge	V _{DD} = 640 V I _D = 6 A V _{GS} = 10 V		33 11 14	47	nC nC nC

SWITCHING OFF

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
`t _f	1	$V_{DD} = 640V$ $I_{D} = 6 A$ $R_{G} = 4.7 \Omega$ $V_{GS} = 10 V$		11 9 16	16 13 23	ns ns ns

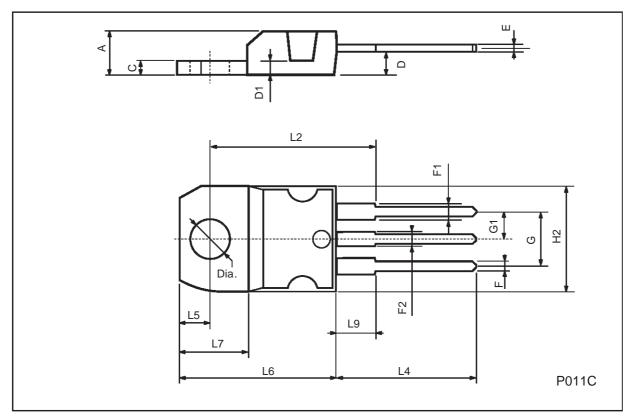
SOURCE DRAIN DIODE

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
I _{SD} I _{SDM} (•)	Source-drain Current Source-drain Current (pulsed)				5.7 22.8	A A
V _{SD} (*)	Forward On Voltage	I _{SD} = 6 A V _{GS} = 0			1.6	V
t _{rr}	Reverse Recovery Time	$I_{SD} = 6 \text{ A}$ di/dt = 100 A/ μ s $V_{DD} = 100 \text{ V}$ $T_i = 150 \text{ °C}$		700		ns
Q _{rr}	Reverse Recovery Charge	,		5.8		μC
I _{RRM}	Reverse Recovery Current			16.5		А

^(*) Pulsed: Pulse duration = 300 μs, duty cycle 1.5 %
(•) Pulse width limited by safe operating area

TO-220 MECHANICAL DATA

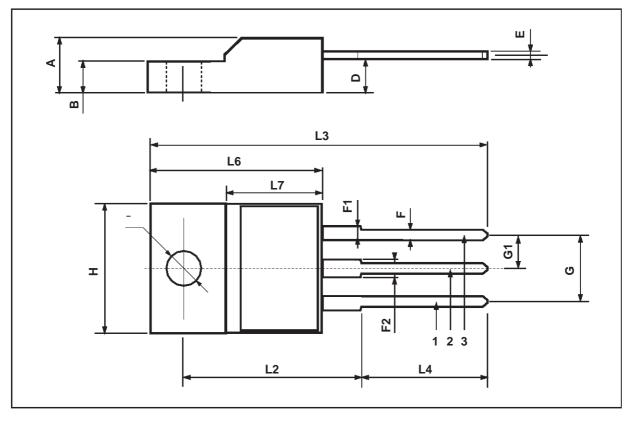
DIM.		mm			inch	
Dilvi.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.40		4.60	0.173		0.181
С	1.23		1.32	0.048		0.051
D	2.40		2.72	0.094		0.107
D1		1.27			0.050	
E	0.49		0.70	0.019		0.027
F	0.61		0.88	0.024		0.034
F1	1.14		1.70	0.044		0.067
F2	1.14		1.70	0.044		0.067
G	4.95		5.15	0.194		0.203
G1	2.4		2.7	0.094		0.106
H2	10.0		10.40	0.393		0.409
L2		16.4			0.645	
L4	13.0		14.0	0.511		0.551
L5	2.65		2.95	0.104		0.116
L6	15.25		15.75	0.600		0.620
L7	6.2		6.6	0.244		0.260
L9	3.5		3.93	0.137		0.154
DIA.	3.75		3.85	0.147		0.151



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TO-220FP MECHANICAL DATA

DIM.		mm			inch	
DIIVI.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А	4.4		4.6	0.173		0.181
В	2.5		2.7	0.098		0.106
D	2.5		2.75	0.098		0.108
Е	0.45		0.7	0.017		0.027
F	0.75		1	0.030		0.039
F1	1.15		1.7	0.045		0.067
F2	1.15		1.7	0.045		0.067
G	4.95		5.2	0.195		0.204
G1	2.4		2.7	0.094		0.106
Н	10		10.4	0.393		0.409
L2		16			0.630	
L3	28.6		30.6	1.126		1.204
L4	9.8		10.6	0.385		0.417
L6	15.9		16.4	0.626		0.645
L7	9		9.3	0.354		0.366
Ø	3		3.2	0.118		0.126



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